

## FORM PTO-1449

**INFORMATION DISCLOSURE CITATION**Attorney Docket:  
238664US25DIV-7057502001Applicant:  
Jack D. PIPPINFiling Date:  
November 7, 2000Application No.:  
09/707,486Examiner:  
Russell W. FREJD  
Group Art Unit:  
2128

AUG 06 2008

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	REF. NO.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS
	CA.	4,786,847	Nov. 22, 1988	DAGGETT <i>et al</i>		
	CB.	4,833,708	May 23, 1989	GOODRICH		
	CC.	5,021,679	Jun. 4, 1991	FAIRBANKS <i>et al</i>		
	CD.	5,086,501	Feb. 4, 1992	DeLUCA <i>et al</i>		
	CE.	5,146,544	Sep. 8, 1992	ALTHAM <i>et al</i>		
	CF.	5,167,024	Nov. 24, 1992	SMITH <i>et al</i>		
	CG.	5,201,059	Apr. 6, 1993	NGUYEN		
	CH.	5,204,863	Apr. 20, 1993	SAINT-JOIGNY <i>et al</i>		
	CI.	5,222,239	Jun. 22, 1993	ROSCH		
	CJ.	5,218,704	Jun. 8, 1993	WATTS, Jr., <i>et al</i>		
	CK.	5,230,055	Jul. 20, 1993	KATZ <i>et al</i>		
	CL.	5,233,161	Aug. 3, 1993	FARWELL <i>et al</i>		
	CM.	5,239,652	Aug. 24, 1993	SEIBERT <i>et al</i>		
	CN.	5,254,888	Oct. 19, 1993	LEE <i>et al</i>		
	CO.	5,283,661	Feb. 1, 1994	KLEES		
	CP.	5,287,292	Feb. 15, 1994	KENNY <i>et al</i>		
	CQ.	5,291,607	Mar. 1, 1994	RISTIC <i>et al</i>		
	CR.	5,710,929	Jan. 20, 1998	FUNG		
	CS.	5,752,011	May 12, 1998	THOMAS <i>et al</i>		
	CT.	5,805,403	Sep. 8, 1998	CHEMLA <i>et al</i>		
	CU.	5,808,438	Sep. 15, 1998	JEFFREY		
	CV.	5,884,068	Mar. 16, 1999	CONARY <i>et al</i>		
	CW.	5,974,557	Oct. 26, 1999	THOMAS <i>et al</i>		
	CX.	6,216,235	Apr. 10, 2001	THOMAS <i>et al</i>		
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	CZ.	7,167,993	Jan. 23, 2007	THOMAS <i>et al</i>		
	CAA.	7,293,186	Nov. 6, 2007	THOMAS <i>et al</i>		

**FOREIGN PATENT DOCUMENTS**

	REF. NO.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
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	BB.	573651	Dec. 15, 1993	EP			English Abstract
	BC.	0821459	Jan. 28, 1998	EP			English Abstract

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	BD.	10-93010	Apr. 10, 1998	JP			English Abstract not available
	BE.	11-48769	Feb. 23, 1999	JP			English Abstract not available

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CC.	S.C. ELLIS, Intel Corporation; <u>THE LOW POWER INTEL486™ SL MICROPROCESSOR</u> ; 1993 IEEE; pps. 96-102.
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CI.	<u>Figure 2. x86 PROCESSOR INTERDUCTIONS: NOW</u> ; COMPCON Feb. 1994; 1-pg.
CJ.	D. STOENNER AMD Application Note; <u>EZ-030 DEMONSTRATION BOARD THEORY OF OPERATION</u> ; Jan. 1994; 15 Pages.
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CN.	B. NADEL <i>et al</i> ; PC Magazine 114, Vol. 13, No. 8, ISSN: 0888-8507; <u>POWER TO THE PC</u> ; April 26, 1994; pps. 1-15.
CO.	PC User; <u>486 MONO NOTEBOOKS</u> ; January 26, 1994; pps. 1-14.
CP.	PC User 37; <u>BATTLE FOR THE DESKTOP: DX4 NOTEBOOKS</u> ; May 4, 1994; pps. 1-4.
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CR.	Windows Sources Vol. 2, No. 1, ISSN: 1065-9641; <u>WELL CONNECTED NOTEBOOKS: FULL SPEED AHEAD</u> ; January 1, 1994; 4 pages.
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CT.	G. PAAP; PowerPC™ <u>A PERFORMANCE ARCHITECTURE</u> ; 1993 IEEE; pps. 104-108.
CU.	Toshiba America Information Systems Inc.; <u>T5100 (no longer manufactured)</u> ; June 2, 1994; 4 pages.
CV.	Toshiba America Information Systems Inc.; <u>T8500 (no longer manufactured)</u> ; June 3, 1994; 3 pages.
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CX.	Toshiba America Information Systems Inc.; <u>T1200F/FB/H/HB (no longer manufactured)</u> ; May 20, 1994; 5 pages.
CY.	Power PC; <u>ADVANCE INFORMATION POWERPC™ 603 RISC MICROPROCESSOR TECHNICAL SUMMARY</u> ; 1994, portions © 1991-94; 30 pages.
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CAA.	National Science Foundation; <u>SUMMARY OF AWARDS FOR THE MIPS DIVISION</u> ; 1993; 120 Pages.
CBB.	Pentium™ Family User's Manual/int.1®, Volume 3; <u>ARCHITECTURE AND PROGRAMMING MANUAL</u> ; 1994; 8 pages.
CCC.	Pentium™ Family User's Manual/int.1®, Volume 1; <u>DATA BOOK</u> ; 1994; 15 pages.
CDD.	Pentium™ Family User's Manual/int.1®, Volume 2; <u>82496/82497 CACHE CONTROLLER AND 82491/82492 CACHE SRAM DATA BOOK</u> ; 1994; 2 pages.
CEE.	HP Virtual Museum: Hewlett-Packard Omnibook 300, 1993 (web page) <u>HP OMNIBOOK 300, 1993-SIX VIEWS</u> ; www.hp.com/hpinfo/about; January 10, 2005; 1-page.
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CHH.	TDB; <u>HIGH POWER LSI PERFORMANCE OPTIMIZER</u> ; January 1991; 2 pages.
CII.	APM Specification, Intel Corporation Microsoft Corporation; Rev. 1.2; <u>ADVANCED POWER MANAGEMENT (APM) BIOS INTERFACE SPECIFICATION</u> ; February 1996; 79 pages.

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